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By: \_\_\_\_\_\_ Date: \_\_\_\_\_ Date: \_\_\_\_\_\_ September 4, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Applicant** 

Gerald Deboy et al.

Appl. No.

10/629,107

Filed

July 29, 2003

Title

Configuration for Generating a Voltage Sense Signal in a Power

Semiconductor Component

**Art Unit** 

## **LETTER**

Hon. Commissioner for Patents, Alexandria, VA 22313-1450

## Sir:

The above-mentioned new patent application was filed on July 29, 2003 without a signed oath or declaration, under the provision of 37 C.F.R. 1.53(f).

In accordance with the above-mentioned rule, enclosed herewith is the original signed declaration.

The undersigned hereby states that the application filed in the Patent and Trademark Office is the application which the inventor(s) executed by signing the declaration. MPEP 602 (8<sup>th</sup> ed., Aug. 2001).

The fee required for the late filing of an oath or declaration in the amount of \$130.00 is also enclosed.

Respectfully submitted,

GREASON L. MAYBACK

RES. NO. 40,716

/mjb

Date: September 4, 2003 Lerner and Greenberg, P.A.

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